

FIG. 1

FIG. 2

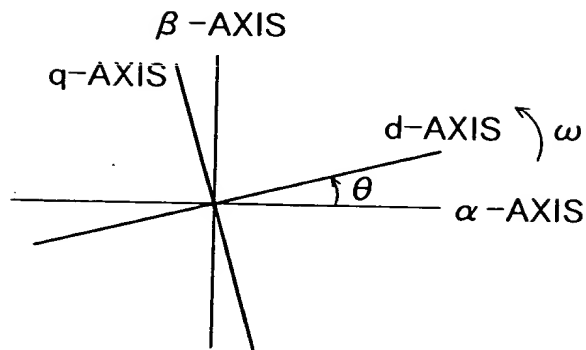
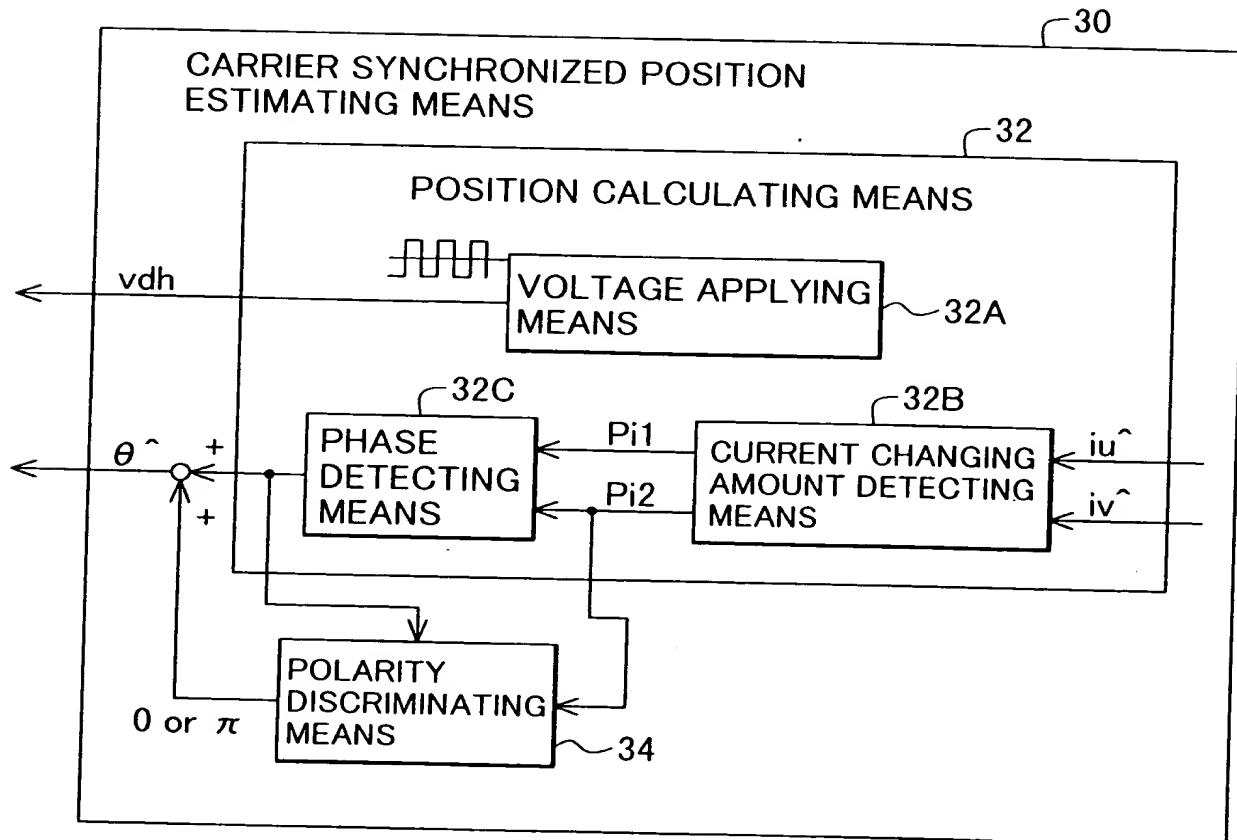


FIG. 3



0925700.081001

FIG.4

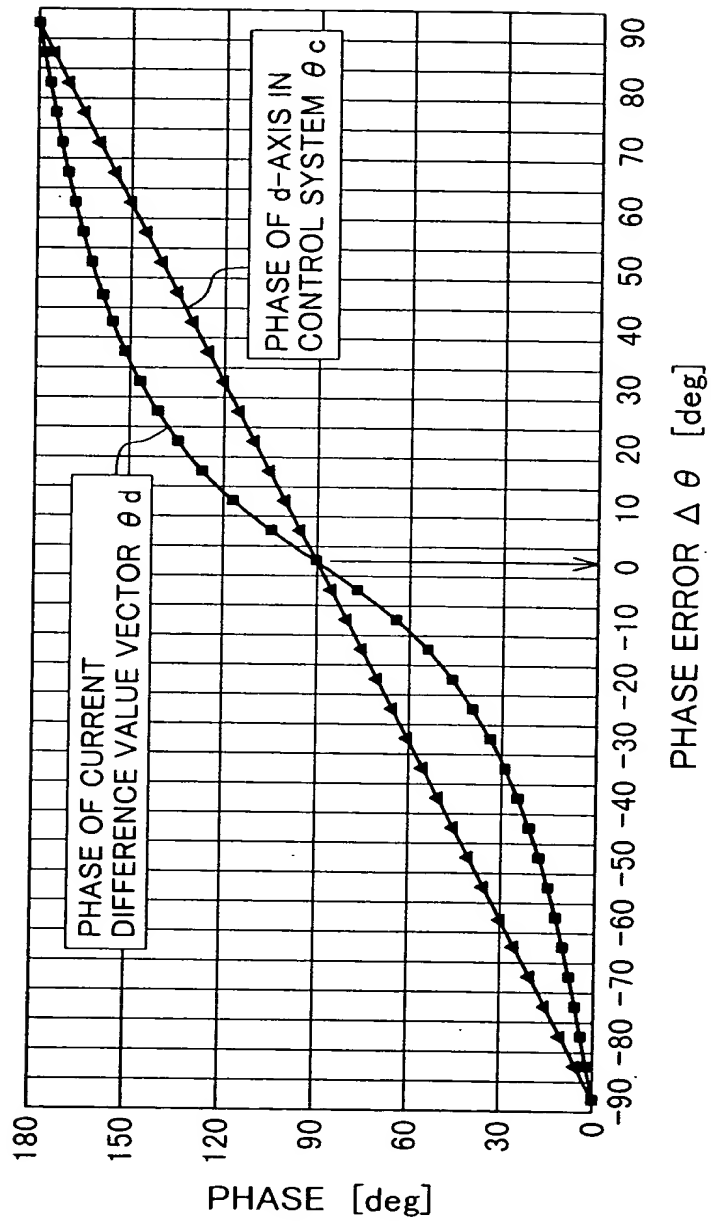
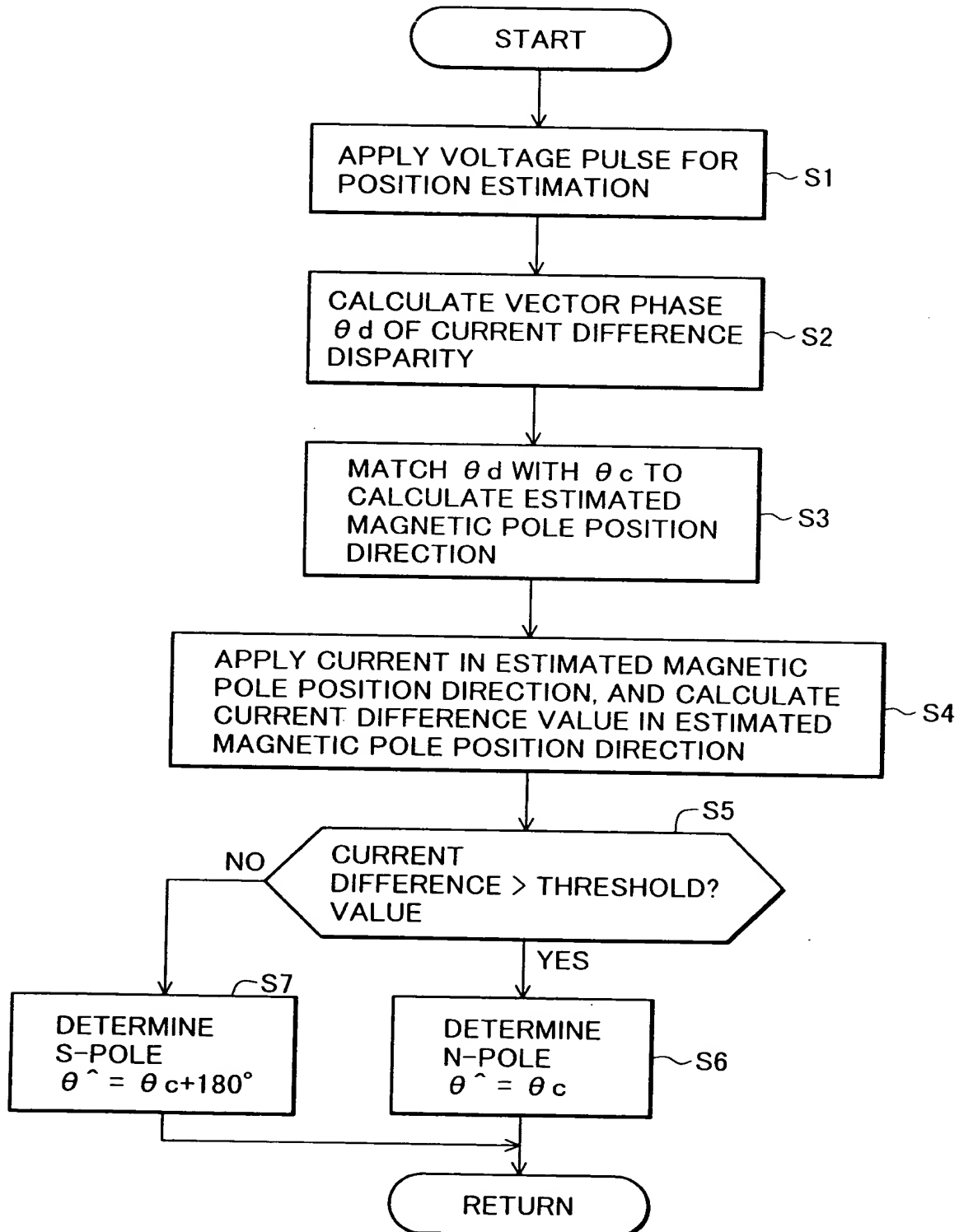


FIG.5



09925700.081001

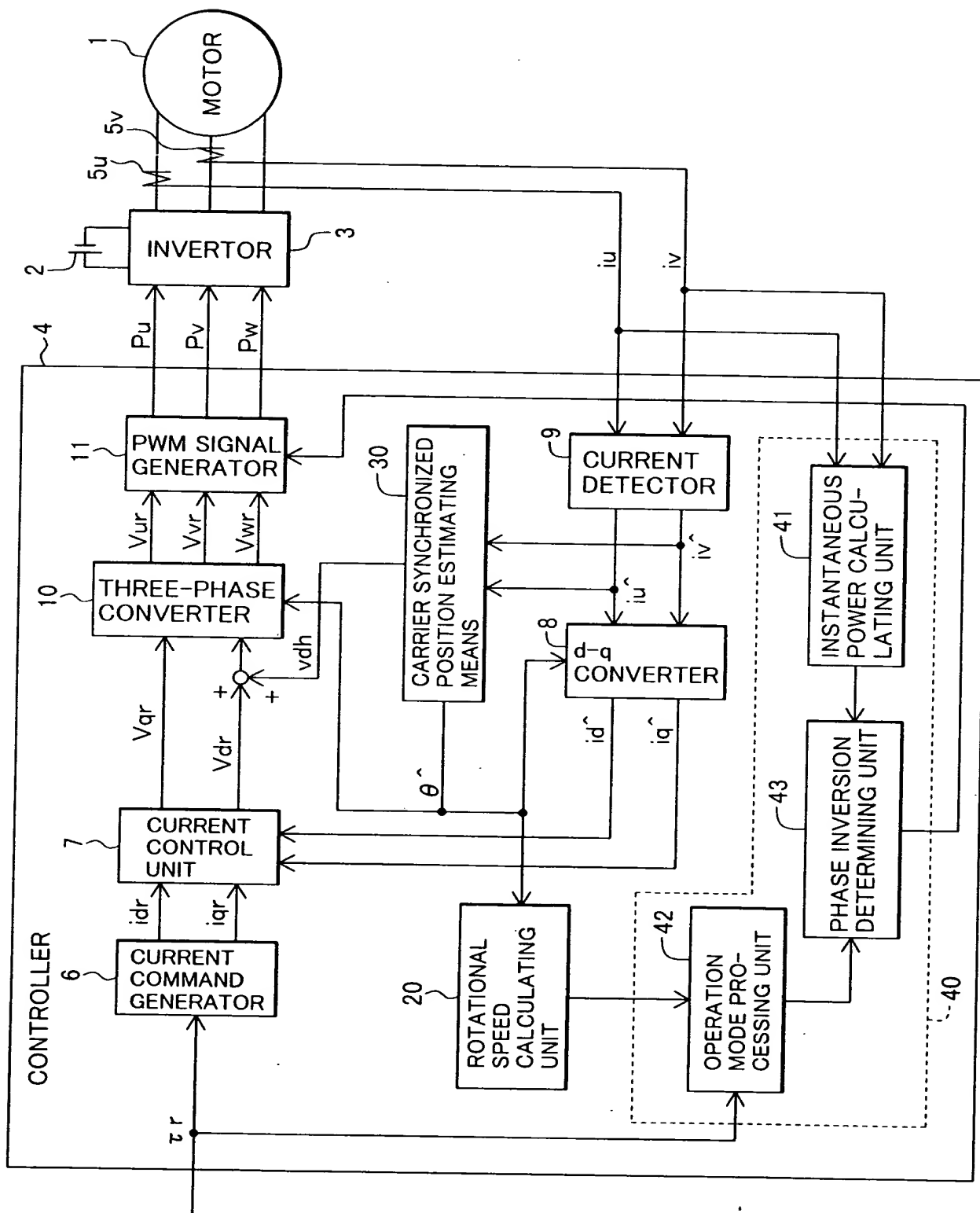
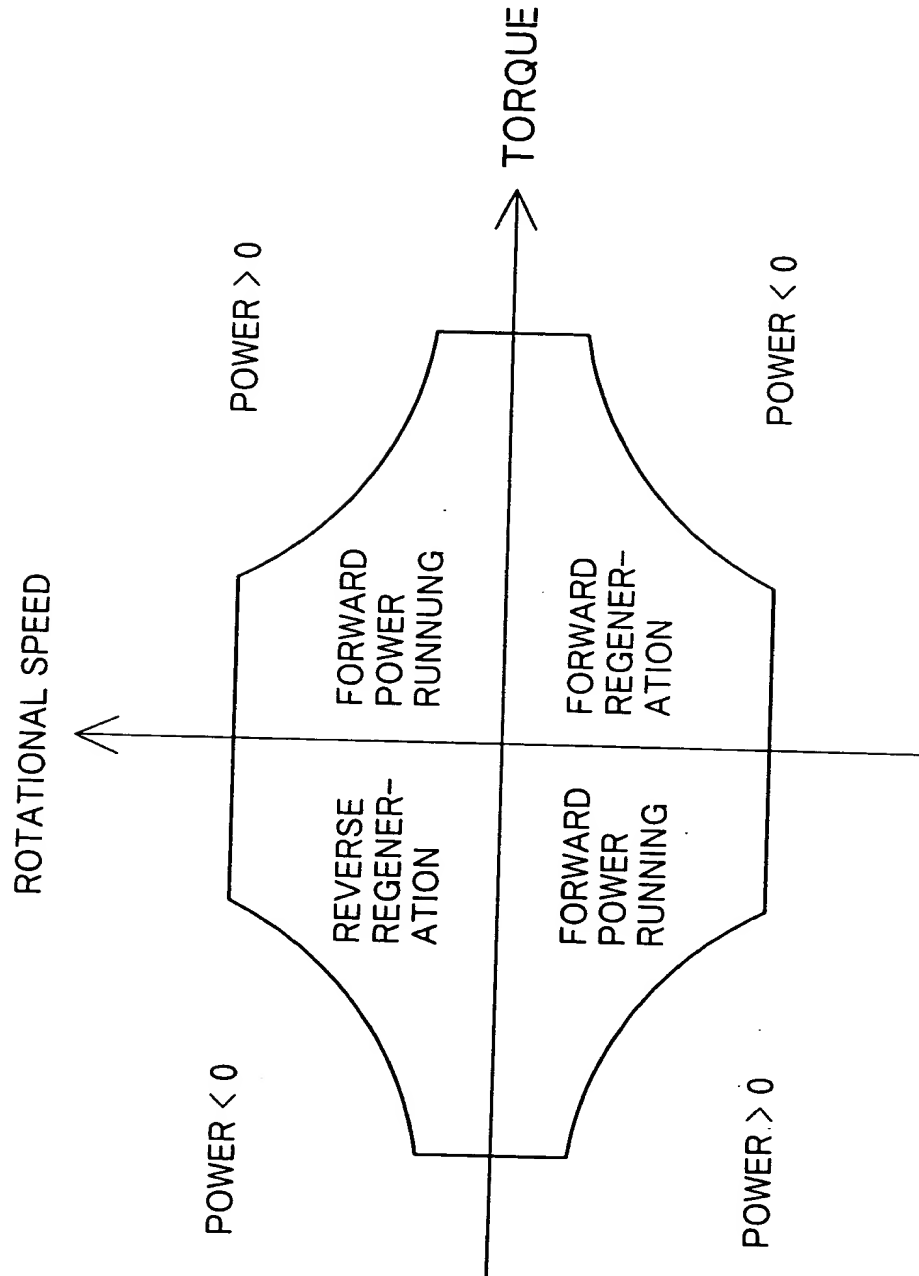


FIG.6

FIG. 6 00252650

FIG.7



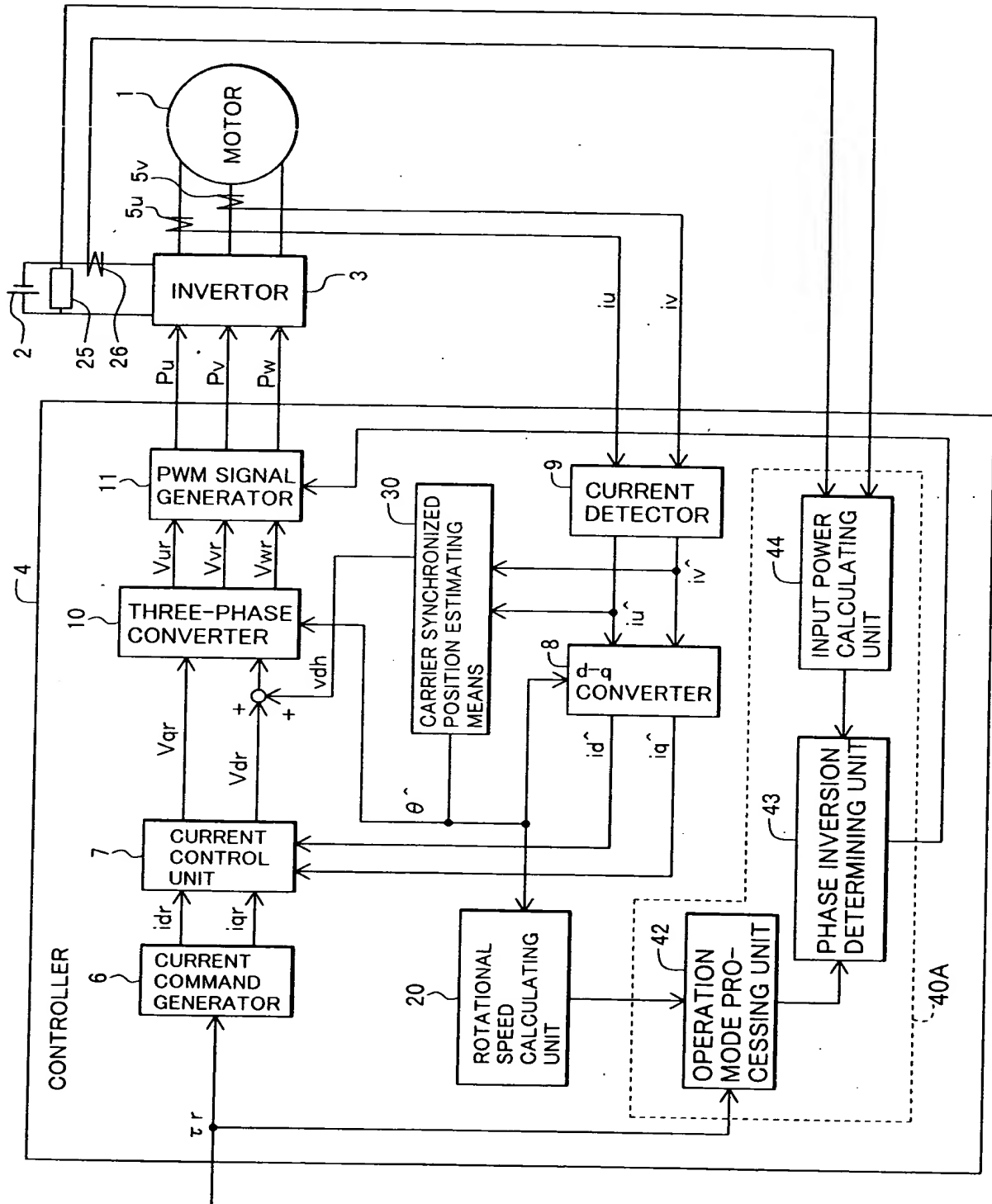


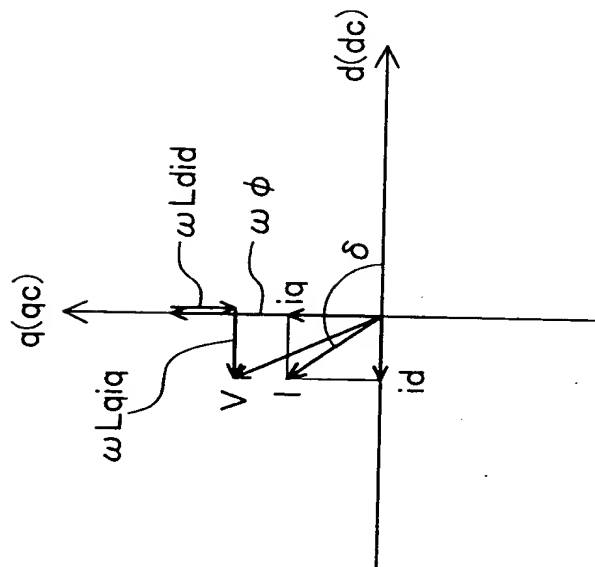
FIG. 8

FIG. 8 00/52660

[illegible]

FIG.10A

FORWARD POWER
 RUNNING



PHASE
 INVERSION

FIG.10B

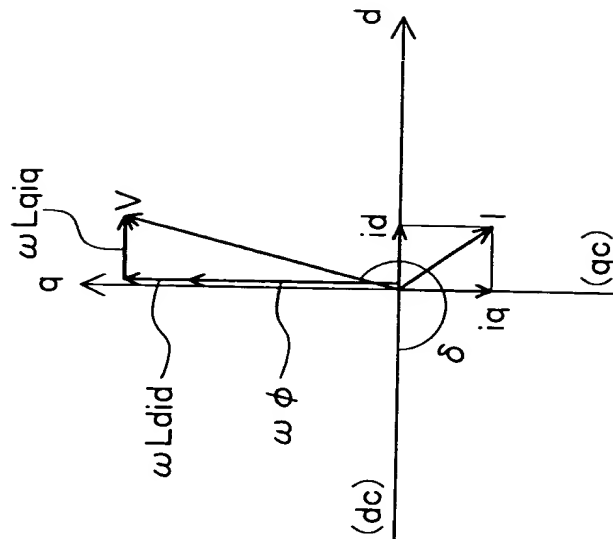
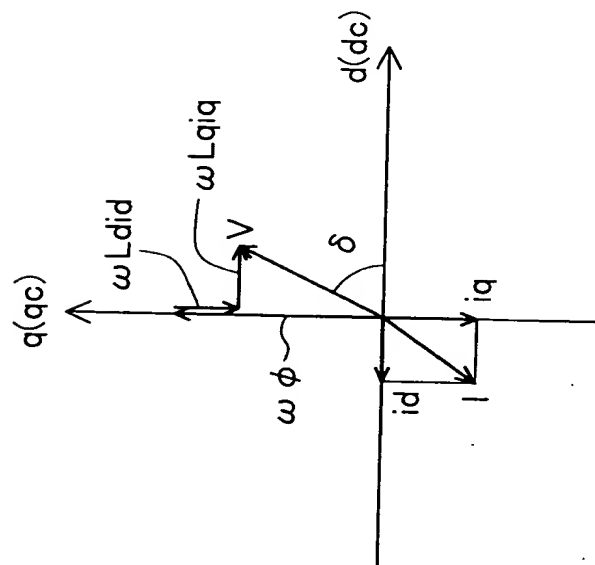


FIG. 11A

FORWARD
REGENERATION



PHASE
INVERSION

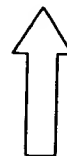


FIG. 11B

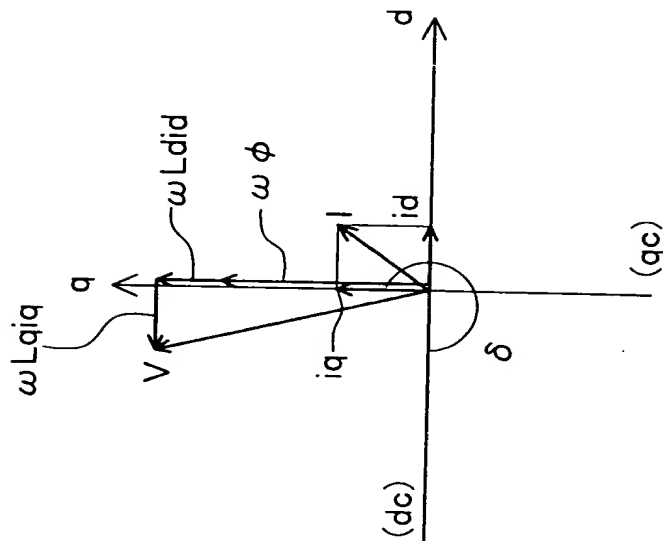
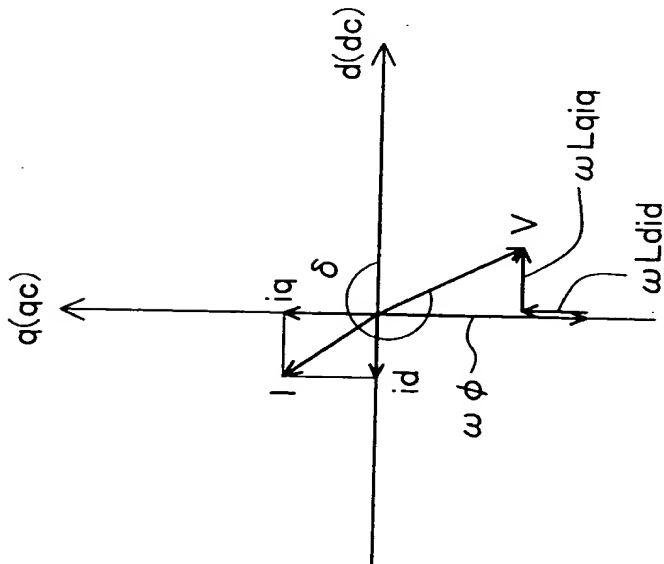


FIG. 12A

REVERSE
REGENERATION



PHASE
INVERSION

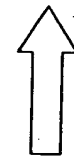


FIG. 12B

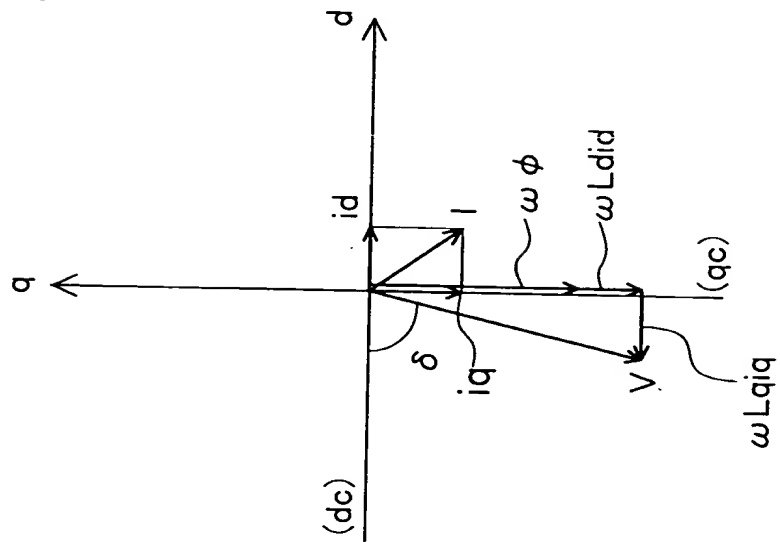
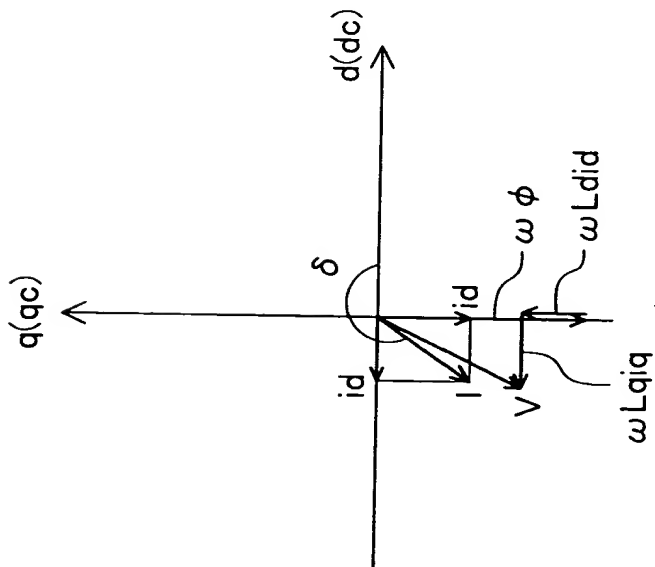


FIG.13A

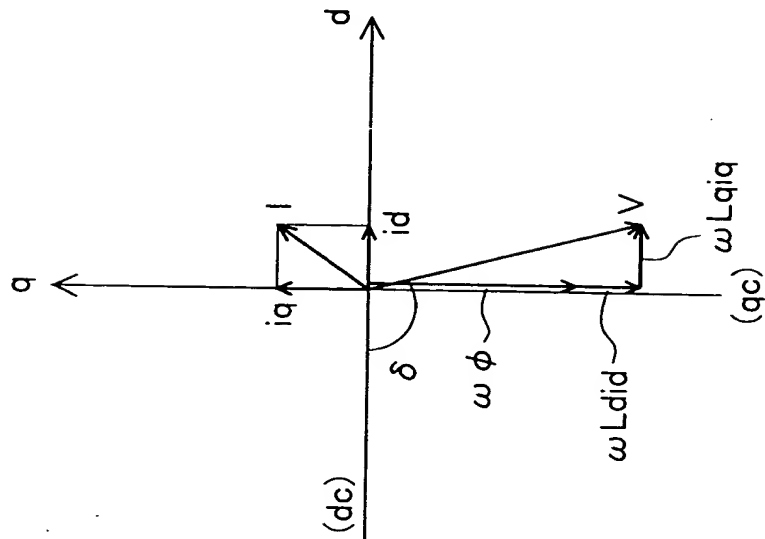
REVERSE
POWER RUNNING



PHASE
INVERSION



FIG.13B



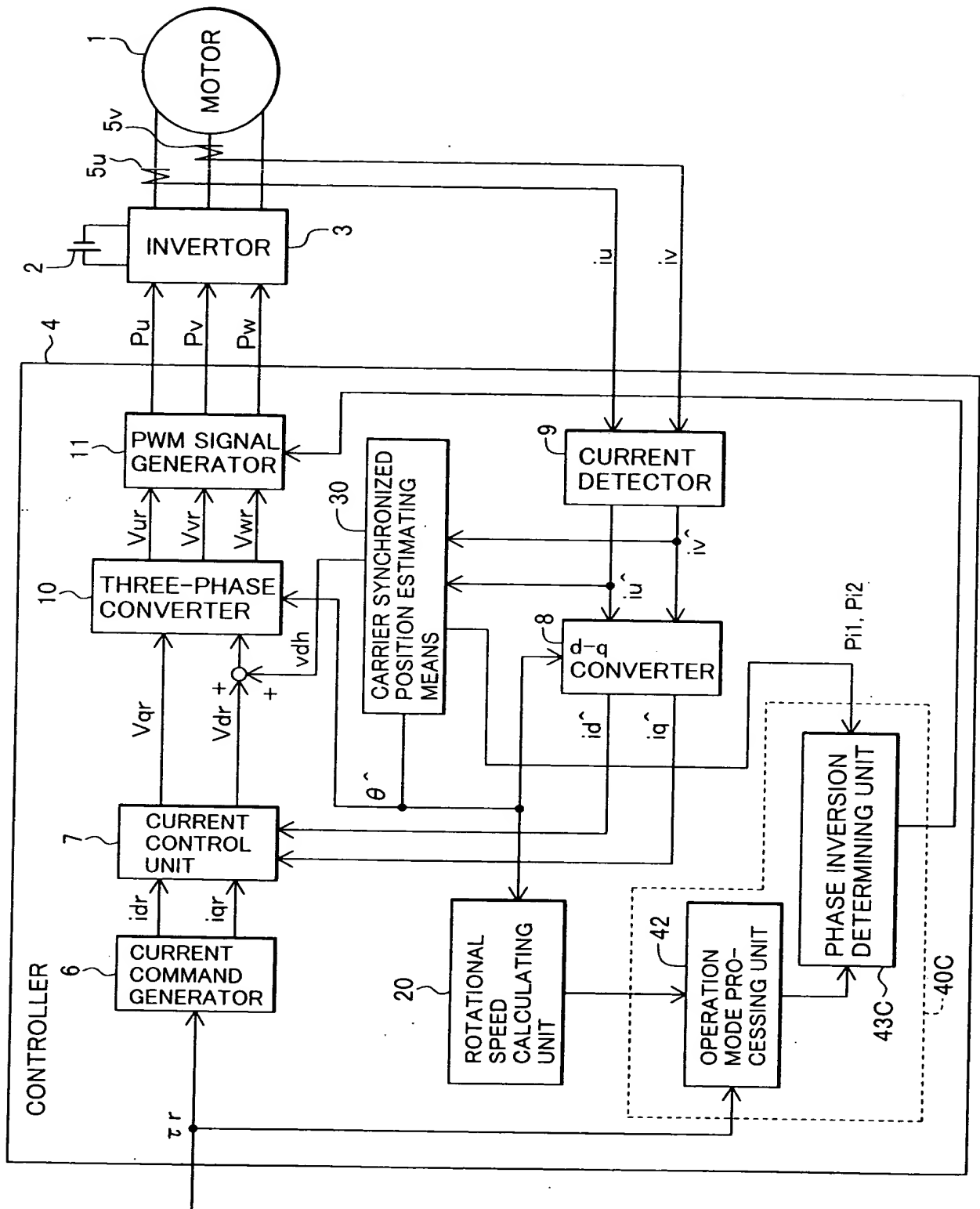
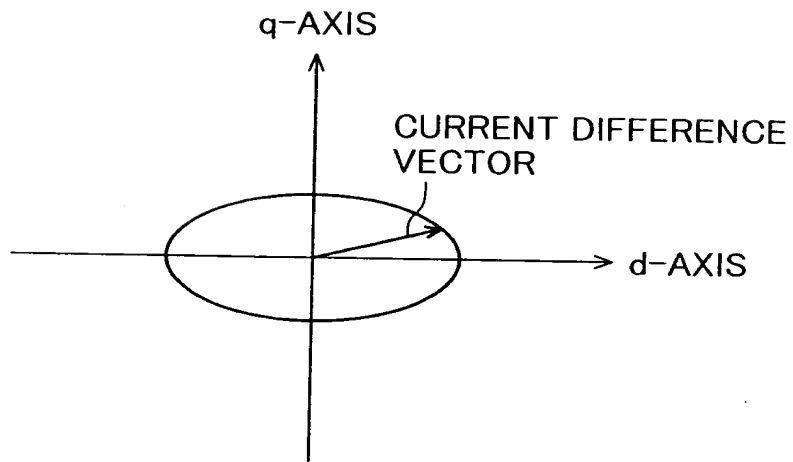


FIG.14

FIG.14 00/52660

FIG.15



0925700.081001

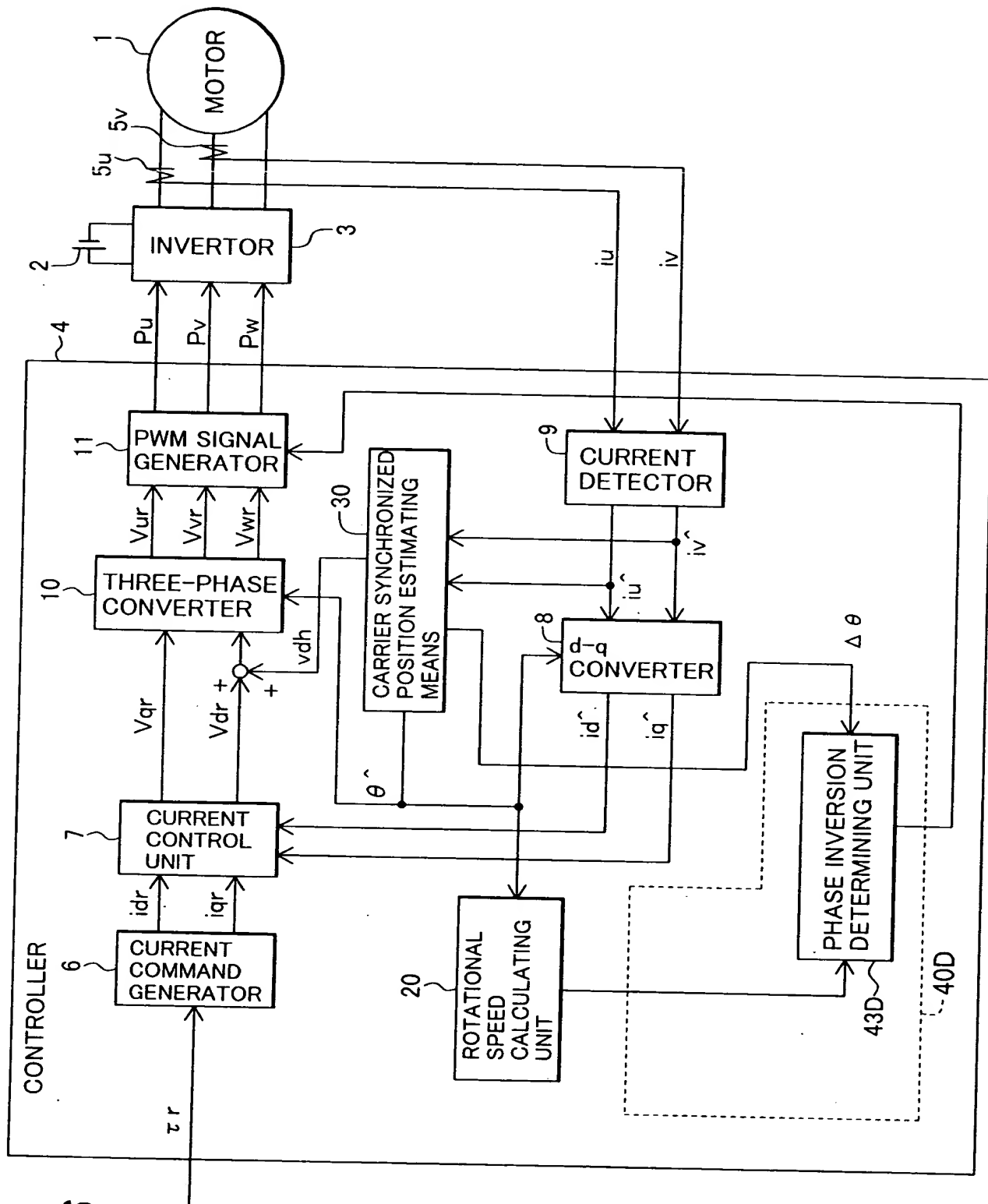
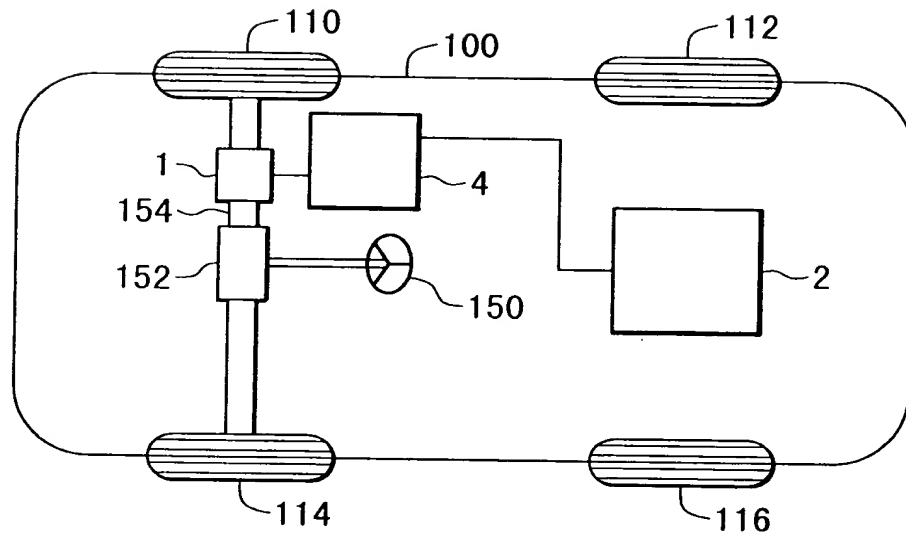


FIG.16

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FIG.17



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